



INFORMATION CITED BY APPLICANT(S) THAT MAY BE MATERIAL TO THE  
PROSECUTION OF THE SUBJECT APPLICATION

Applicant: Jee-Soo Mok, Byung-Kook Sun, Attorney Docket No. LEPA122042  
Chang-Kyu Song, Geum-Hee Yun,  
Tae-Hoon Kim

Application No.: *(filed concurrently)* Group Art Unit:

Filed: November 24, 2003 Examiner:

Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING IMPROVED  
INTERCONNECTION AND METHOD FOR MANUFACTURING THE SAME

U.S. PATENT DOCUMENTS

*Examiner Cite	Kind	Date	
Initials No. Document No. Code (mm/dd/yyyy) Name			
None			

FOREIGN PATENT DOCUMENTS

*Examiner Cite	Kind	Publication Date	Country	English Abstract Provided	Translation Provided
Initial No. Document No. Code (mm/dd/yyyy)					
F1	WO 01/39267	A1	5/31/2001	Japan	X

OTHER INFORMATION

(Including Author, Title, Date, Pertinent Pages, Etc.)

*Examiner Cite	
Initial No.	
None	

Examiner Date Considered

\*Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

JEN:hjd

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